Amendments to the Specification:

Please replace paragraph **[0031]** with the following amended paragraph:

~~Te~~ The invention provides a system for transferring time restricted data over a jitter including channel, the system including: (a) a retriever, coupled to a buffer, for retrieving the time restricted data from the buffer, at a retrieval rate; (b) a buffer level monitor, coupled to the buffer, for monitoring the level of time restricted data in the buffer at a monitoring rate; and (c) a controller coupled to the buffer level monitor and to the retriever, for setting the retrieval rate and the monitoring rate. Conveniently, this system further includes at least one of the following entities: (a1) a decapsulator, connected to said buffer, wherein said decapsulator extracts said time restricted data from bursty channel format packets and wherein said decapsulator provides said time restricted data to said buffer; (a2) a receiving end communication interface, connected to said decapsulator, wherein said receiving end communication interface receives said bursty channel format packets from said bursty communication channel, and wherein said receiving end communication interface provides said bursty channel format packets to said decapsulator; (a3) a transmitting end communication interface, for transmitting said bursty channel format packets to said receiving end communication interface over said bursty communication channel; (a4) an encapsulator, connected to said transmitting end communication interface, for encapsulating said time restricted data in said bursty channel format packets; (a5) a time restricted data source, connected to said encapsulator; (a6) a communication unit, coupled to the retriever. Preferably, the retriever is further connected to a communication unit such as a decoder, for decoding said time restricted data, a transmitter, for transmitting said time restricted data to a remote receiver; and/or a multiplexer, for multiplexing said time restricted data.

Please replace paragraph **[0036]** with the following amended paragraph:

In general, an MPEG receiving unit such as MPEG decoder 112, includes an internal PLL unit ~~(not shown)~~, which has to be restrictively synchronized with the PLL of the MPEG transport transmitter. Using a bursty communication channel in the chain of communication, induces delays which are beyond the synchronicity restrictions of the MPEG decoder PLL unit.

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Please replace paragraph **[0045]** with the following amended paragraph:

Reference is now made to Figure 3, which is a schematic illustration of a method for operating system 100 of Figure 2, operative in accordance with another preferred embodiment of the present invention. In step 160, the buffer level is monitored. With reference to Figure 2, buffer level monitor 106 monitors the currently used storage area within FIFO buffer 132. In step 162, the buffer level behavior is detected to determine if it complies with a given behavior pattern ~~(can you give some examples)~~. It the buffer behavior diverts from that behavior pattern, then the method proceeds to step 164, when the buffer level exhibits a generally rising pattern and to step 166, when the buffer level exhibits a generally falling pattern. With reference to Figure 2, controller 104 analyzes the behavior of the buffer level, according to the readings provided thereto by buffer level monitor 106.